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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/713,572
	Filing Date	11/12/2003
	First Named Inventor	Kazuaki Sakurada
	Art Unit	1762
	Examiner Name	Brian K. Talbot
Total Number of Pages in This Submission	Attorney Docket Number	9319G-000593

ENCLOSURES (check all that apply)		
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Firm or Individual name	Harness, Dickey & Pierce, P.L.C.	Attorney Name G. Gregory Schivley Bryant E. Wade	Reg. No. 27,382 40,344
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Date	May 30, 2007		

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/713,572

Filing Date: 11/12/2003

Applicant: Kazuaki Sakurada

Group Art Unit: 1762

Examiner: Brian K. Talbot

Title: MANUFACTURING METHOD OF MULTILAYER
CIRCUIT BOARD (as amended)

Attorney Docket: 9319G-000593

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

AMENDMENT

Sir:

In response to the Office Action mailed March 21, 2007, please amend the application as follows and consider the remarks set forth below.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 3 of this paper.

Remarks begin on page 6 of this paper.